

Camera Interface Board

- ▲ Low Power
- ▲ Compact design
- ▲ PAL Video Output
- ▲ GigE Vision Compatible
- ▲ Xilinx Zynq FPGA/Processor
- ▲ Upto 24-bit Parallel Video Input
- ▲ RS485 Serial Interface for commands
- ▲ Built-in Shutter Motor Driver for IR Cores
- ▲ Debug Interface (Serial, Parallel)
- ▲ GigE Vision Control Protocol (GVCP)
- ▲ GigE Vision Stream Protocol (GVSP)



The STIRA GigE Vision converter is an interface board that takes parallel video input (can support Camera Link input) and converts it into a GigE Vision standard. The converter can stream video at maximum resolution 1024x768 and at maximum 110 FPS. It supports all video types including Mono8 and Mono16. It can output a test pattern video for debugging and it has packet delay support. Finally, this converter is compatible with Pleora eBUS Player.

Technical Specifications

Technical Specifications	STES-CIB
Processor	Xilinx Zynq 7020 with ARM Cortex-A9 @ 667 Mhz, and Artix-7 FPGA with 85K logic
RAM	DDR3 2048 MB
Ethernet	1000/100/10 Mbps
Flash	QSPI 16 MB (check size)
Video out	GigE Vision, Analog
UART	RS232/RS485
Operating Temperature	-20° to 95°C (Optional)
Operating System	Bare-metal (no OS)
Display Layers	Optional on Custom needs
Codec Support	Optional on Custom needs
GigE Vision	Support Mono8 at 1024x768 resolution and up to 110 FPS, All standard pixel modes up to 24 bit @1080p30.
Boot Time (Configuration to Application).	Less than 5 seconds
Dimensions	65mm x 65mm

Power

Electrical Power Consumption	7 Watts
Operating Voltage	24 Volts DC
Operating Current (Nominal)	<300 mAmps

Interface

All Electrical Interface	Straight Harwin Connectors for Harsh Environments, Standard Connectors (Optional)
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